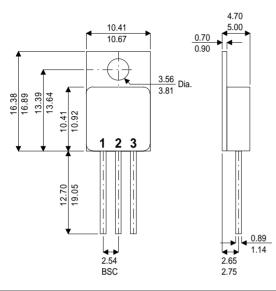


TO-220 Isolated Metal Package



Pinouts						
Pin	Fixed	Adjustable				
1	GROUND	ADJUST				
2	OUTPUT	OUTPUT				
3	INPUT	INPUT				
Case is ISOLATED						

Ordering Information					
LT1085–220M	Positive Adjustable Regulator				
LT1085-05-220M	Fixed +5V Regulator				
LT1085-12-220M	Fixed +12V Regulator				

LT1085-220M LT1085-05-220M LT1085-12-220M

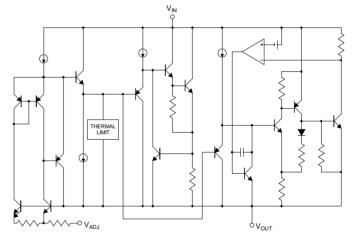
MILITARY VERSION

LOW DROPOUT 3 AMP POSITIVE VOLTAGE REGULATOR

FEATURES

- Low Dropout Performance
- Fixed or Adjustable Voltages
- Fixed Output Voltages of 5V & 12V
- Output Current 3A
- Line Regulation 0.015% / V Typical.
- Load Regulation 0.1% Typical.
- Military Temperature Range (-55 to +150°C)

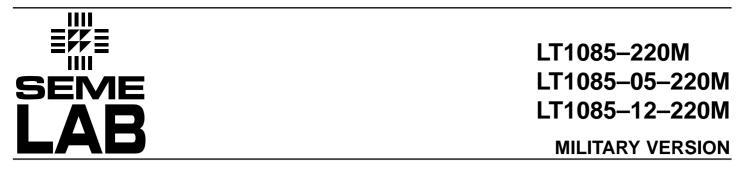
BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^{\circ}C$ unless otherwise stated)

		ase		
V _{IN}	Input Voltage	5V, 12V		30V
V _{I-O}	Input–Output Differential Voltage	Adjustable		35V
PD	Power Dissipation			Internally limited
V _{IN}	Operating Input Voltage	Adjustable 5 Volt 12 Volt		25V 20V 25V
Т _Ј	Operating Junction Temperature Range		Control Power	–55 to 150°C −55 to 200°C
T _{STG}	Storage Temperature Range			–65 to 150°C
T _{LEAD}	Lead Temperature (for 10 sec.)			300°C

* Although the devices' maximum operating voltage is limited (20V for the 5V device, and 25V for the 12V and adjustable devices) the devices are guaranteed to withstand transient input voltages up to 30V. For input voltages greater than the maximum operating input voltage, some degradation of specifications will occur. For 5V and 12V devices operating at input-output voltage differentials >15V, a minimum external load of 5mA is required to maintain regulation.



DESCRIPTION

The LT1085 is designed to provide 3A with higher efficiency than currently available devices. All internal circuitry is designed to operate down to 1V input to output differential and the dropout voltage is fully specified as a function of load current. Dropout is guaranteed at a maximum of 1.5V at maximum output current, decreasing at lower load currents. On-chip trimming adjusts the reference output voltage to 1%. Current limit is also trimmed, minimising the stress on both the regulator and power source circuitry under overload conditions.

The LT1085 is pin compatible with older 3 terminal adjustable regulators. A 10µF output capacitor is required on these new devices; however, this is usually included in most regulator designs.

Unlike PNP regulators, where up to 10% of the output current is wasted as quiescent current, the LT1085 quiescent current flows into the load, increasing efficiency.

	Parameter	Output Voltage	Test Conditions		Min.	Tyn	Max.	Unit	
	Farameter		$V_{IN} = 6.5$ to 20V	$I_{O} = 0$ to 3A		тур.			
Vo	Output Voltage	5	$T_{\rm J} = -55$ to 150°C	0	4.90	5	5.10	V	
		12	V _{IN} = 13.5V to 25V	$I_{O} = 0$ to 3A	11.76	12	12.24		
			T _J = –55 to 150°C						
M	Reference Voltage	ADJ.	$V_{IN} - V_{OUT} = 1.5$ to 15V	$I_{O} = 10$ mA to 3A	1.225	1.25 1.270			
V _{REF}			T _J = –55 to 150°C		1.220		1.270		
REG _(LINE)		5	V _{IN} = 6.5 to 20V			0.5	10	- mV	
			I _O = 0	$T_{\rm J} = -55$ to 150°C		1	10		
	Line Regulation	12	V _{IN} = 13.5 to 25V			1	25		
			I _O = 0	$T_{\rm J} = -55$ to $150^{\circ}{\rm C}$		2	25		
		ADJ.	$V_{IN} - V_{OUT} = 1.5$ to 15V			0.015	0.2	%	
			I _{LOAD} = 10mA	$T_{\rm J} = -55$ to $150^{\circ}{\rm C}$		0.035	0.2		
		5	V _{IN} = 8V		5 2		20		
			$I_{O} = 0$ to 3A	$T_{\rm J} = -55 \text{ to } 150^{\circ}\text{C}$		10	35	- 	
550	Load Regulation	12	V _{IN} = 15V			12	36	mV	
REG _(LOAD)			$I_{O} = 0$ to 3A	$T_{\rm J} = -55$ to $150^{\circ}{\rm C}$		24	72	1	
	See notes 1,2	ADJ.	$V_{IN} - V_{OUT} = 3V$			0.1	0.3		
			I _O = 10mA to 3A	$T_{\rm J} = -55 \text{ to } 150^{\circ}\text{C}$		0.2	0.4	- %	
V _D		5	$\Delta V_{OUT} = 50 mV$	1	1.3		1.5		
		5	I _{OUT} = 3A	$T_{J} = -55$ to $150^{\circ}C$		1.5	1.5		
	Dropout Voltage	12	$\Delta V_{OUT} = 120 mV$			1.3	1.5	v	
			I _{OUT} = 3A	$T_{J} = -55$ to $150^{\circ}C$		1.0	1.0		
	See note 3	ADJ.	$\Delta V_{REF} = 1\%$			1.3	1.5		
			I _{OUT} = 3A	$T_{J} = -55$ to $150^{\circ}C$		1.3	1.5		

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise stated)



LT1085–220M LT1085–05–220M LT1085–12–220M

MILITARY VERSION

ELECTRICAL CHARACTERISTICS ($T_J = -55$ to +150°C unless otherwise stated)

	Parameter	Output Voltage	Test Conditions		Min.	Тур.	Max.	Unit
1	Current Limit		$V_{IN} - V_{OUT} = 5V$	T _J = -55 to 150°C	3.2	4		Α
I _{CL}			$V_{IN} - V_{OUT} = 25V$	$T_{J} = -55$ to 150°C	0.2	0.5		$\left \begin{array}{c} 1 \end{array} \right $
I _Q (Quiescent Current	5	$V_{IN} \le 20V$	$T_{\rm J} = -55$ to $150^{\circ}{\rm C}$		5	10	
		12	$V_{IN} \le 25V$	$T_{\rm J} = -55 \text{ to } 150^{\circ}\text{C}$		5	10	mA
	Minimum Load Current ⁴	ADJ.	$V_{IN} - V_{OUT} = 25V$	$T_J = -55$ to $150^{\circ}C$		5	10	
REG _{(TF}	HERM) Thermal Regulation	•	T _P = 30ms	$T_A = 25^{\circ}C$		0.004	0.02	%/W
		5	T _J = −55 to 150°C	$V_{IN} = 8V$	60	68		
R _A	Ripple Rejection	12	$C_{OUT} = 25 \mu F Tantalum$	V _{IN} = 15V	54	60		dB
		ADJ.	f = 120Hz	$V_{IN} - V_{OUT} = 3V$	60	75		
			I _O = 3A	$C_{ADJ} = 25 \mu F$	00			
	Adjust Pin Current	ADJ.				55		μA
I _{PIN}				$T_{\rm J} = -55 \text{ to } 150^{\circ}\text{C}$			120	μΑ
ΔI_{PIN}	Adjust Pin Current Change	ADJ.	$V_{IN} - V_{OUT} = 1.5$ to 15V			0.2	5	μΑ
			I _O = 10mA to 3A	$T_J = -55$ to $150^{\circ}C$				
Τ _S	Temperature Stability		T _J = −55 to 150°C			0.5		%
	Long Term Stability		T _A = 125°C	T = 1000 Hrs		0.3	1	%
V _N	RMS Output Noise		f = 10Hz to 10kHz	T _A = 25°C		0.003		%
$R_{ extsf{ heta}JC}$	Thermal Resistance			Control Circuitry	ТВА			°C/W
	Junction – Case			Power Transistor		TBA		

Notes:

1. See thermal regulation specifications for changes in output voltage due to heating effects. Load and line regulation are measured at a constant junction temperature by low duty cycle pulse testing. Load regulation is measured at the output lead $\approx 1/8$ " from the package.

2. Line and load regulation are guaranteed up to the maximum power dissipation of 30W. Power dissipation is determined by the input - output differential and the output current. Guaranteed maximum power dissipation will not be available over the full input - output voltage range.

3. Dropout voltage is specified over the full output current range of the device.